

ENA EREC G98/NI:2019

Type Approval and Manufacturer declaration of compliance with the requirements of G98/NI.

This form should be used when making a Type Test submission to the Energy Networks

Association (ENA).

If the Micro-generator is Fully Type Tested and already registered with the ENA Type Test Verification Report Register, the Installation Document should include the Manufacturer's Reference Number (the Product ID), and this form does not need to be submitted.

Where the Micro-generator is not registered with the ENA Type Test Verification Report

Register this form needs to be completed and provided to the DNO, to confirm that the Microgenerator has been tested to satisfy the requirements of this EREC G98/NI.

SSEG Type	reference number	HM-800,HM-700,HM-600				
SSEG Type		Photovoltaic	Microinverter			
System Supp	plier name	Hoymiles Po	ower Electronics I	nc.		
Address		No.18 Kangjing road, HangZhou, Zhejiang Provin P.R. China				
Tel	+86 571 28056101		Fax	+86 571 28056137		
E:mail	zhangxingyao@hzo m	converter.co	Web site	www.hoymiles.com		
Maximum rated		Con	nnection Option			
capacity, use	0.8/0.7/0.6	kW single ph	nase, single, split o	r three phase system		
separate sheet if	NA	kW three pha	ase			
more than one	NA	kW two phas	kW two phases in three phase system			
connection option.	NA	kW two phases split phase system				

SSEG manufacturer/supplier declaration

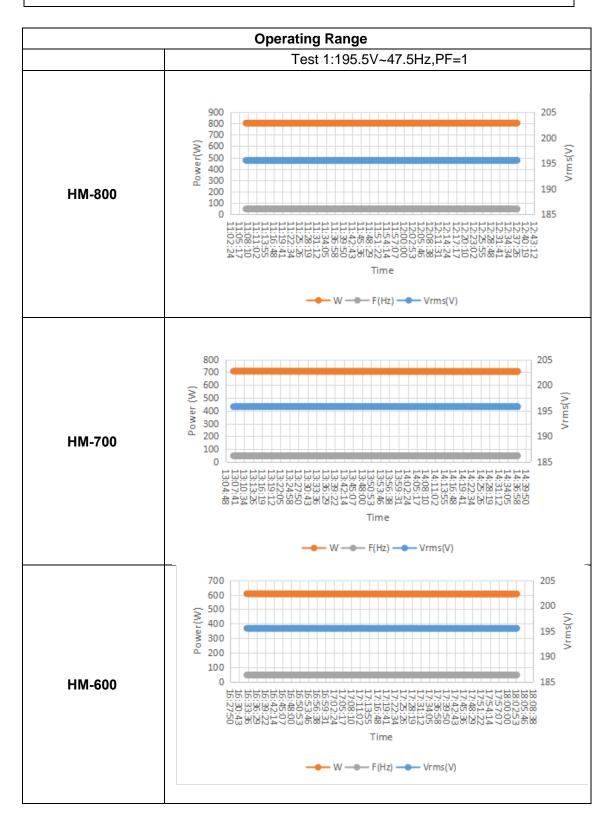
Manufacturer Type Test declaration. - I certify that all products supplied by the company with the above Type Tested reference number will be manufactured and tested to ensure that they perform as stated in this document, prior to shipment to site and that no site modifications are required to ensure that the product meets all the requirements of EREC G98/NI.

Signed 建岩湖	On behalf of	Hoymiles Power Electronics Inc.
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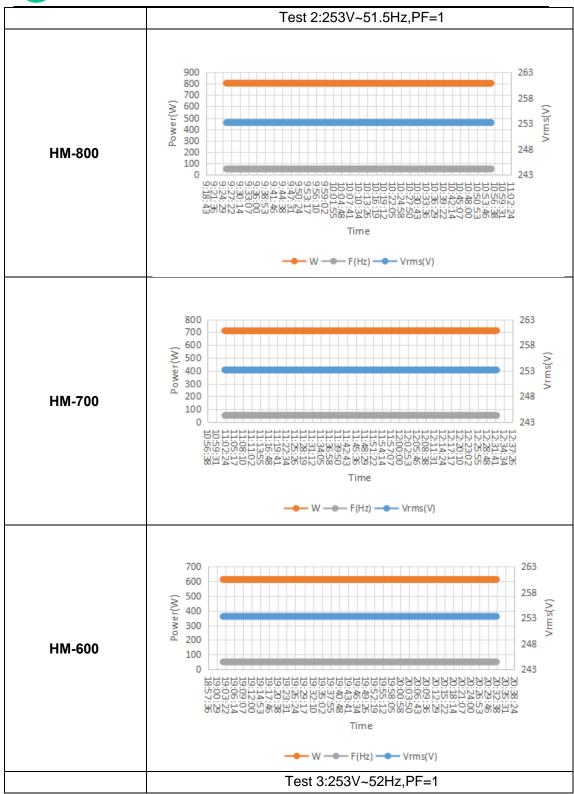


Note that testing can be done by the Manufacturer of an individual component or by an external test house.

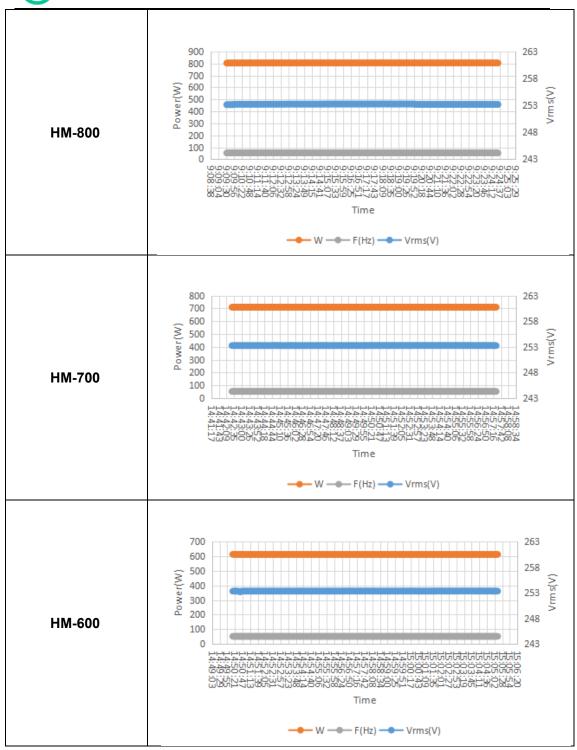
Where parts of the testing are carried out by persons or organisations other than the Manufacturer then that person or organisation shall keep copies of all test records and results supplied to them to verify that the testing has been carried out by people with sufficient technical competency to carry out the tests.







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Power (Power Quality – Harmonics: These tests should be carried out as specified in BS EN 61000-3-2.									
SSE	EG rating per phase (rpp)	kW	NIV / NAV/*2 CO/mag							
Harmo nic	At 45-55% of rated output	100% of ra	ated output	NV=MV*3.68/rpp						

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	Measured Value(MV) in Amps	Normalised Value (NV) In Amps	Measured Value(MV) In Amps	Normalised Value (NV) In Amps	Limit in BS EN 61000-3- 2 in Amps	Higher limit for odd harmonic s 21 and above
2	0.0745	0.4578	0.1490	0.8480	1.080	
3	0.0218	0.1360	0.0536	0.2204	2.300	
4	0.0119	0.0715	0.0288	0.0695	0.430	
5	0.0079	0.0487	0.0209	0.0387	1.140	
6	0.0070	0.0447	0.0169	0.0387	0.300	
7	0.0070	0.0397	0.0139	0.0258	0.770	
8	0.0060	0.0338	0.0119	0.0209	0.230	
9	0.0040	0.0258	0.0109	0.0129	0.400	
10	0.0040	0.0228	0.0089	0.0209	0.184	
11	0.0040	0.0218	0.0089	0.0179	0.450	
12	0.0040	0.0209	0.0079	0.0149	0.153	
13	0.0040	0.0228	0.0070	0.0179	0.210	
14	0.0030	0.0159	0.0079	0.0129	0.131	
15	0.0020	0.0119	0.0099	0.0238	0.150	
16	0.0020	0.0119	0.0060	0.0079	0.115	
17	0.0020	0.0119	0.0089	0.0129	0.132	
18	0.0020	0.0139	0.0109	0.0149	0.102	
19	0.0010	0.0070	0.0030	0.0179	0.118	
20	0.0010	0.0040	0.0040	0.0129	0.092	
21	0.0020	0.0099	0.0050	0.0010	0.107	
22	0.0020	0.0099	0.0030	0.0179	0.084	
23	0.0030	0.0199	0.0030	0.0070	0.098	0.147
24	0.0030	0.0189	0.0020	0.0070	0.077	

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25	0.0020	0.0119	0.0060	0.0079	0.090	0.135
26	0.0020	0.0119	0.0030	0.0010	0.071	
27	0.0020	0.0129	0.0030	0.0179	0.083	0.124
28	0.0020	0.0109	0.0030	0.0179	0.066	
29	0.0020	0.0149	0.0030	0.0139	0.078	0.117
30	0.0010	0.0079	0.0030	0.0149	0.061	
31	0.0020	0.0149	0.0040	0.0119	0.073	0.109
32	0.0010	0.0060	0.0040	0.0010	0.058	
33	0.0010	0.0089	0.0030	0.0209	0.068	0.102
34	0.0010	0.0040	0.0030	0.0070	0.054	
35	0.0030	0.0159	0.0030	0.0139	0.064	0.096
36	0.0030	0.0189	0.0000	0.0149	0.051	
37	0.0020	0.0109	0.0030	0.0050	0.061	0.091
38	0.0000	0.0020	0.0030	0.0099	0.048	
39	0.0010	0.0060	0.0030	0.0050	0.058	0.087
40	0.0010	0.0060	0.0010	0.0179	0.046	

Note the higher limits for odd harmonics 21 and above are only allowable under certain conditions, if these higher limits are utilised please state the exemption used as detailed in part 6.2.3.4 of BS EN 61000-3-2 in the box below.

Power Quality. Voltage fluctuations and Flicker.												
		Starting			Stopping	9	Running					
	dmax [%]	dc [%]	d(t) [%]	dmax [%]	dc [%]	d(t) [%]	Pst	Plt 2 hours				
Measured Values	0.36	0.3	0	0.36	0.3	0	0.133	0.058				
Normalised to standard impedance and 3.68kW for multiple units	2.21	1.84	0	2.21	1.84	0	0.82	0.36				



Limits set under BS EN 61000-3-2	4%	3.30%	3.3% 500ms	4%	3.30%	3.3% 500ms	1	0.65
Test start date	2020-07-31			Test en	d date	2020-07	7 -31	
Test location						TION INS		

Power quality – DC injection: This test should be carried out in accordance with EN 50438 Annex D.3.10									
Test power level	20%	50%	75%	100%					
Recorded value(mA)	0.016	0.056	0.875	2.952					
as % of rated AC	0.004%	0.004%	0.025%	0.085%					
Limit	0.25%	0.25%	0.25%	0.25%					

Power Quality – **Power factor:** This test shall be carried out in accordance with EN 50538 Annex D.3.4.1 but with nominal voltage -6% and +10%. Voltage to be maintained within ±1.5% of the stated level during the test.

	216.2V	230V	253V
20% of Registered	0.9887	0.9864	0.9887
50% of Registered	0.9953	0.9946	0.9936
75% of Registered	0.9967	0.9955	0.9948
100% of Registered	0.9975	0.9963	0.9959
Limit (Leading)	>0.95	>0.95	>0.95
Limit (Lagging)	>0.98	>0.98	>0.98

Protection. Frequency tests These tests should be carried out in accordance with EN 50438 Annex D.2.4 and the notes in EREC G98/NI Annex A1 A 1.3.2 (Inverter connected) or Annex A2 A.2.2.2 (Synchronous)

Function Setting	Trip test	"No trip tests"
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	Frequency	Time delay	Frequency	Time delay	Frequency /time	Confirm no trip
U/F	48Hz	0.5s	48Hz	0.53s	48.2Hz/ 25s	Confirm
					47.8Hz/ 0.45s	Confirm
O/F	52Hz	1.0s	52Hz	1.04s	51.8Hz/120s	Confirm
					52.2Hz/ 0.98s	Confirm

Protection. Voltage tests These tests should be carried out in accordance with EN 50438 Annex D.2.3 and the notes in EREC G98/NI Annex A1 A 1.3.1 (Inverter connected) or Annex A2 A.2.2.1 (Synchronous)

Function	Setti	ng	Trip t	est	"No trip tests"			
	Voltage	Time delay	Voltage	Time delay	Voltage /time	Confirm no trip		
U/V stage 1	195.5V	3.0s	195.8V	3.06s	199.5V/5.0s	Confirm		
U/V stage 2	138V	2.0s	137.7V	2.03s	142V2.5s	Confirm		
					134V/1.95s	Confirm		
O/V stage 1	253V	0.5s	252.9V	0.52s	249V/5.0s	Confirm		
		257V/0.45s	Confirm					

Note for Voltage tests the Voltage required to trip is the setting ±3.45 V. The time delay can be measured at a larger deviation than the minimum required to operate the protection. The No trip tests need to be carried out at the setting ±4 V and for the relevant times as shown in the table above to ensure that the protection will not trip in error.

Protection. Loss of Mains test. For PV Inverters shall be tested in accordance with BS EN62116. Other Inverters should be tested in accordance with EN 50438 Annex D.2.5 at 10%,55% and 100% of rated power.

Note: Inverter tested according to BS EN 62116.

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Test Power and	33%	66%	100%	33%	66%	100%
imbalance	-5% Q	-5% Q	-5% P	+5% Q	+5% Q	+5% P
Trip time. Limit is 0.5s	45.13ms	411.2ms	325.2ms	57.23ms	411.5ms	336.5ms



Protection. Frequency change, Stability test This test should be carried out in accordance with EREC G98/NI Annex A1 A 1.3.5 (Inverter connected) or Annex A2 A.2.2.5 (Synchronous).

	Start Frequency	Change	End Frequency	Confirm no trip
Positive Vector Shift	49.5Hz	+50 degrees		Confirm
Negative Vector Shift	50.5Hz	- 50degrees		Confirm

Protection – **Frequency change, RoCoF Stability test:** The requirement is specified in section 11.3, test procedure in Annex A 1.3.5 (Inverter connected) or Annex A2 A.2.2.5 (Synchronous).

	Ramp range	Test frequency	Test Duration	Confirm no trip
Positive Frequency drift	49Hz to 51Hz	+0.95Hz/sec	2.1s	Confirm
Negative Frequency drift	51Hz to 49Hz	-0.95Hz/sec	2.1s	Confirm

Protection – **Limited Frequency Sensitive Mode** – **Overfrequency test:** This test should be carried out in accordance with EN 50438 Annex D.3.3 Power response to over- frequency. The test should be carried out using the specific threshold frequency of 50.2 Hz and dro op of 10%.

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Test sequence	Measured	Frequency	Primary Power	Active Power
at Registered	Active Power		Source	Gradient
Capacity >80%	Output			
Step a) 50.00	000 2111	50Hz		-
Hz ±0.01Hz	800.3W			
Step b) 50.25	700.014	50.25Hz		-
Hz ±0.05Hz	790.9W			
Step c) 50.70	745 4101	50.7Hz		-
Hz ±0.10Hz	745.1W			
Step d) 51.15	000 011	51.15Hz		-
Hz ±0.05 Hz	663.3W			
Step e) 50.70	745 211	50.7Hz		-
Hz ±0.10Hz	745.3W			
Step f) 50.25	704 4\\	50.25Hz		-
Hz ±0.05Hz	791.1W			
Step g) 50.00	000 414/	50Hz		-
Hz ±0.10Hz	800.1W			
Test sequence	Measured	Frequency	Primary Power	Active Power
at Registered	Active Power		Source	Gradient
Capacity	Output			
40% - 60%				
Step a) 50.00	406.7W	50Hz		-
Hz ±0.01Hz	400.7 00			
Step b) 50.25	397.1W	50.25Hz		-
Hz ±0.05Hz	397.100			



Step c) 50.70 Hz ±0.10Hz	352.3W	50.7Hz	-
Step d) 51.15 Hz ±0.05 Hz	269.9W	51.15Hz	-
Step e) 50.70 Hz ±0.10Hz	352.5W	50.7Hz	-
Step f) 50.25 Hz ±0.05Hz	397.6W	50.25Hz	-
Step g) 50.00 Hz ±0.10Hz	406.9W	50Hz	-
Steps as define	ed in EN 50438		

Protection – Power output with falling frequency test: This test should be carried out in accordance with EN 50438 Annex D.3.2 active power feed -in at under-frequency.				
Test sequence	Measured Active Power Output	Frequency	Primary power source	
Test a) 50 Hz ± 0.01 Hz	800.13W	50Hz	DC supply	
Test b) Point between 49.5 Hz and 49.6 Hz	800.08W	49.55Hz	DC supply	
Test c) Point between 47.5 Hz and 47.6 Hz	800.02W	47.55Hz	DC supply	
NOTE: The operating	point in Test (b) and (c) shall be maintained fo	or at least 5 minutes	

Protection. Re-connection timer.					
Test should prove that the reconnection sequence starts after a minimum delay of 20 s for restoration of voltage and frequency to within the stage 1 settings of Table 2.					
Time delay setting	Measured delay	No reconnection when voltage or frequency is brought to just outside stage 1 limits of table 1.			
60s	90s	At 257V	At 191.5V	At 47.9Hz	At 52.1Hz
Confirmation the		Confirm Confirm Confirm			Confirm

Fault level contribution. The requirement is specified in section 5.7, test procedure in Annex A or B 1.4.6					
For a directly coupled SSEG			Fo	or a Inverter SS	EG
Parameter	Symbol	Value	Time after fault	Volts	Amps
Peak Short Circuit current	ip	N/A	20ms	19.25V	0.153A



Initial Value of aperiodic	А	N/A	100ms	10.26V	0.121A
Initial symmetrical	lk	N/A	250ms	9.24V	0.116A
Decaying (aperiodic)	iDC	N/A	500ms	5.98V	0.109A
Reactance/Re sistance Ratio	X/R	N/A	Time to trip	0.0032	(in seconds)

Self-Monitoring solid state switching: No specified test requirements. Refer to EREC G98/NI Annex A1 A 1.4.6 (Inverter connected).	Yes/or NA
It has been verified that in the event of the solid state switching device failing to disconnect the Micro-generator, the voltage on the output side of the switching device is reduced to a value below 50 V within 0.5 s.	N/A

Logic interface (input port)	Yes/or NA
Confirm that an input port is provided and can be used to shut down the	Voc
module.	Yes

Additional comments		
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